

ABSTRACT

An objective is to provide a microcomputer, electronic
equipment and emulation method which can realize the optimum
5 circumstance of evaluation while saving the number of terminals.
An external bus is shared between external and emulation
memories. In the emulation mode, the access of CPU to an internal
ROM is switched to the access of CPU to the emulation memory
through an external bus. The emulation mode is turned ON or OFF
10 through a mode selection terminal or mode selection register.
The emulation memory is controlled by a control signal CNT2
different from a control signal CNT1 which controls the external
memory. A memory read signal in CNT2 become active at a timing
earlier than that of a memory read signal in CNT1. Thus, the
15 instruction is fetched and decoded within one clock cycle. A
mode selection terminal is further provided for selecting a mode
of performing the boot from the emulation memory, internal ROM
or external memory and a mode of selecting OPT mode.